Senior Staff / Senior Electrical Engineer (FPGA)

Permanent Job

Location: Bayan Lepas, Penang

Job Description:

- Responsible for the design & development of highly-sophisticated communication products such as Base Station, Router and Console products.
- Responsible for micro-architecture, design, simulation, verification and test activities associate with FPGA development. Responsible for the chipset selection to achieve performance and cost goals.
- Design and implement industry standard digital communications protocols.
- Provide technical leadership and guidance to junior engineers in project execution and technical issue resolutions.
- Work with a team of highly-qualified engineers across the globe (electrical, mechanical, software) to generate innovative ideas and solutions that meet customer requirements.

Job Requirements:

- Bachelor’s Degree or master’s degree in Electrical/Electronics/Computer Engineering with at least 6 years’ experience in product design and development (minimum 8 years for Staff position).
- Hands on experience with FPGA design flow from Verilog RTL design, simulation, verification, synthesis and static timing analysis.
- Experience in Xilinx/Altera softcore processor architecture and design implementation is required.
- Experience in implementing industry standard interfaces like Ethernet and USB.
- Experience with Source Control Management tools (like Sourceforge GIT) is an added advantage.
- Familiar with hardware development flow from schematic entry, layout, timing analysis to design verification and validation. Experience in Cadence design flow is a plus.
- Experience in C programming and firmware development is a strong plus.
- Good command of written and spoken English.
- Must be willing to work in Bayan Lepas, Penang